

FreeRTOS on Hercules Devices

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ABSTRACT

FREERTOS is a popular open-source real-time operating system used in embedded systems. It provides APIs for tasks, semaphores, mutexes, timers, and so forth.

HALCoGen is a GUI-based driver generating tool for the Hercules family of devices. HALCoGen also supports FREERTOS for various devices in the Hercules family. It enables users to generate the FREERTOS code, along with other drivers.

This application report provides an overview on FreeRTOS. This document is not a FreeRTOS user's guide, and only describes how to use FreeRTOS on Hercules devices with HALCoGen.

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FreeRTOS Source Files

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1 FreeRTOS Source Files

FreeRTOS source files can be divided into platform-dependent and platform-independent files. It supports various families, including TI's Hercules microcontroller.

The FreeRTOS\Source folder contains the drivers for each submodule, such as tasks, semaphores, queues, and so forth. These drivers are platform-independent.

The FreeRTOS\Source\include folder contains the header files, including a header file named mpu_wrappers.h. For the platforms that support MPU, these header files redefine the kernel functions to the corresponding MPU wrapper functions. More details on MPU wrapper functions are provided in the following sections.

The FreeRTOS\Source\portable\XXX folder contains platform-dependent code. For every platform, FREERTOS provides a .c and an assembly file for port functions, and a header file named portmacro.h for the platform-dependent macros.

Along with these source files, a FreeRTOS project requires the following files:

- FreeRTOSConfig.h Contains macros for customizing the FreeRTOS drivers. Sample file provided as part of demos.
- mpu_wrappers.c Contains the MPU wrapper functions for kernel functions. For FreeRTOS v9 and later, a sample file is provided as part of FreeRTOS\Source\portable\Common folder.

2 FreeRTOS Support in HALCoGen

To generate FreeRTOS code along with HALCoGen drivers, select DeviceName_FREERTOS as Device while creating the HALCoGen project, as shown in Figure 1. FreeRTOS drivers are not supported for every device variant in a device family. This document provides steps on how to customize the drivers for a different device variant.

🔳 New Projec	t	x
Family: TMS47 TMS57 TMS57 TMS57 TMS57	0M 0LS20x 0LS31x 0LS21x E	Device: TMS570LS3137ZWT TMS570LS3137PGE TMDX570LS31USB TMDX570LS31HDK
 RM48x TMS57 TMS57 TMS57 RM46x TMS57 TMS57 TMS57 TMS57 TMS57 TMS57 	0LS12x 0LS11x 0LS04x 0LS03x 0LS02x	TMS570LS31372WT_FREERTOS TMS570LS3135PGE TMS570LS3135ZWT TMS570LS3134ZWT TMS570LS3134PGE
Name:	<enter name=""></enter>	
Location:	D:\CCS7Workspace	
	Create directory for projec	t.
Project will	be created at: D:\CCS7Works	space\ <entername>.</entername>
Tools:	Texas Instruments Tools	•
		OK Cancel

The FreeRTOS source files are generated with a prefix "os_".

Figure 1. Select Device



Navigate to the OS tab to configure various options for the OS files, as shown in Figure 2.

Start Page TMS570	LS3137ZWT_FREERTOS	OS	PINMUX	GIO	SCI	LIN	SCI2	MIBSP11		
General										
Configuration Configuration option	ns will set macros in FreeR	TOSC	onfig.h							
🔽 Use Task Preer	mption 📃 Use Mute	exes		V	Use Vert	ose St	ack Che	cking		
Use Idle Hook	📃 Use Rec	ursive	Mutexes		Use Time	ers				
Use Tick Hook	Use Cour	nting S	emaphores		Generate	e Runtir	ne Statis	tics		
Use Co-Routine	s 🔽 Idle Task	Shoul	d Yeild		Use Mall	oc Faile	ed Hook			
🔲 Use Trace Faci	lity 📃 Use Stac	k Ove	flow Hook							
Task Configuration -										
RTI Clock (Hz):	9000000		Tick Rate	(Hz):	1000					
Max Priorities:	5		Total Heap Size:			8192				
Task Name Length:	16		Min Stack	Size:	128					
Coroutine Configurati	on									
Coroutine Priorities:	2									
Timers Configuration										
Timer Task Priority:	0 Queue Length:	0	Stack S	Size:	0					

Figure 2. OS Tab

These configurations are translated to create the FreeRTOSConfig.h, and are provided as part of the generated drivers.

3 MPU Settings as an Add-on for Safety Use Cases

For devices with a Memory Protection Unit (MPU), FreeRTOS provides hooks for MPU configurations. MPU lets the user partition the device memory into various regions and configure the access rights for each regions. TI's Hercules microcontrollers are Arm[®] Cortex[®]-R based and support MPU.

3.1 MPU Wrapper Functions

There are two modes of CPU: privileged mode and user mode (names may differ depending on the device). User mode is a restricted mode, and is typically used to execute the user tasks. Privileged mode is used to execute the kernel code and for system initialization. The kernel code and data are typically placed in a privileged memory section, so that these are not accessed directly by user tasks. MPU wrapper functions provide wrappers for kernel functions so that they are executed in privileged mode.

Sample MPU wrapper function:

```
void MPU_vTaskDelete ( TaskHandle_t xTaskToDelete )
{
    BaseType_t xRunningPrivileged = prvRaisePrivilege();
    vTaskDelete ( xTaskToDelete );
    portRESET_PRIVILEGE( xRunningPrivileged );
}
```

3.2 MPU Regions for Tasks

The FreeRTOS task driver provides support to have n number of memory regions assigned to each task. The memory regions and its access rights may vary, depending on the tasks.

In a FreeRTOS environment, the MPU regions can be categorized into task-specific regions and common regions. Task-specific regions are configured every time a task switch occurs. The common regions are configured at the time the scheduler is initialized.

The first four regions and the last region (regions 0-3 and n-1) are configured when the scheduler is started and are common for all the tasks. This includes the flash, RAM, and peripherals memories. The other regions are task-specific, and can be configured during the task creation. There is a dedicated MPU region for the task stack.

Table 1 lists the device families with the number of MPU regions, and how they are split into common and task-specific regions.

Device Family	No. of MPU Regions	Common Regions	Task-Specific Regions
TMS570LS31x RM48x TMS570LS12x RM46x	12	Region 0: Unprivileged flash region Region 1: Privileged flash region Region 2: Privileged RAM region Region 3: Peripherals region Region 11: Privileged system region	Region 4: Task stack Regions 5-10: User-defined
TMS570LS04x RM42x	8	Region 0: Unprivileged flash region Region 1: Privileged flash region Region 2: Privileged RAM region Region 3: Peripherals region Region 7: Privileged system region	Region 4: Task stack Regions 5,6: User-defined
TMS570LC43x RM57x	16	Region 0: Unprivileged flash region Region 1: Privileged flash region Region 2: Privileged RAM region Region 3: Peripherals region Region 15: Privileged system region	The number of task-configurable regions can be changed in the HALCoGen GUI. Default = 3 Region 11: Task stack Regions 12-14: User-defined The remaining regions can be configured in the HALCoGen GUI, and are common for all tasks.

Table 1. Device Families

NOTE: Privileged flash and RAM regions include the privileged code and data section used by the kernel.

All the MPU configurations done through the HALCoGen GUI are reset when the scheduler is initialized. With TMS570LC43x and RM57x devices, there is an option to set the number of regions used by the FreeRTOS. The regions not used by FreeRTOS can be configured through the HALCoGen GUI. These are not reset by the FreeRTOS code.

To modify the default MPU regions set by FreeRTOS, modify the prvSetupDefaultMPU function available on the os_port.c file.

A task can be created using two FreeRTOS APIs:

- xTaskCreate This disables all user-defined task-specific regions, and enables access to the entire RAM region as part of the task stack region. The task created using this API is called a non-restricted task, as this has access to the entire RAM region.
- xTaskCreateRestricted This lets the user define the regions they need access to. The task stack
 region is set with the actual stack size. The task created using this API is called a restricted task, as
 this does not have access to any RAM region other than its own stack (unless explicitly mentioned).

For more details on how to create restricted and non-restricted tasks, see the HALCoGen examples.

4 Porting FREERTOS on a Different Part Number

HALCoGen supports FreeRTOS driver generation for only a few devices. This section explains how to generate FreeRTOS drivers for any device part number. This example uses the TMS570LS1227PGE device. For the TMS570LS12x family, HALCoGen provides a FreeRTOS port for the TMS570LS1227ZWT part.

1. Create a HALCoGen project with TMS570LS1227ZWT_FREERTOS, as shown in Figure 3.

New Project	t	
Family: TMS47 TMS57 TMS57 RM48x FMS57 RM48x RM46x RM46x TMS57 TMS57 TMS57 TMS57 TMS57	00M Image: Constraint of the second	Device: TMS570LS1227ZWT TMS570LS1227PGE TMS570LS1227ZWT_FREERTOS TMS570LS1225ZWT TMS570LS1225PGE TMS570LS1224ZWT TMS570LS1224PGE TMS570LS1224PGE TMS570LS1224PGE_FREERTOS
Name:	1227ZWT_FreeRTOS	
Location:	D:\CCS7Workspace	
	Create directory for projec	t
Project will	be created at: D:\CCS7Works	space\1227ZWT_FreeRTOS.
Tools:	Texas Instruments Tools	•
		OK Cancel





Porting FREERTOS on a Different Part Number

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 Configure the PLL and other clock settings as per the actual device requirements. In the example shown in Figure 4, the maximum CPU clock for the TMS570LS1227PGE is 160 MHz, whereas, with the TMS570LS1227ZWT, it is 180 MHz.



Figure 4. Configure PLL

3. Configure the FreeRTOS port as required in the OS tab. Save and generate code.



4. Create a HALCoGen project with the TMS570LS1227PGE, as shown in Figure 5.

💽 New Projec	t		x		
Family: TMS47 TMS57 TMS57 TMS57 RM48x FMS57 RM48x RM46x RM46x TMS57 TMS57 TMS57 TMS57 TMS57	0M 0LS20x 0LS31x 0LS21x 0LS12x 0LS12x 0LS11x 0LS04x 0LS03x 0LS03x 0LS02x •	Device: TMS570LS1227ZWT TMS570LS1227PGE TMS570LS1227ZWT_FREERTOS TMS570LS1225ZWT TMS570LS1225PGE TMS570LS1224PGE TMS570LS1224PGE TMS570LS1224PGE_FREERTOS			
Name:	1227PGE				
Location:	D:\CCS7Workspace				
	Create directory for project	t			
Project will	be created at: D:\CCS7Work:	space\1227PGE.			
Tools: Texas Instruments Tools					
		OK Cance	ا العام العام الع العام العام الع		

Figure 5. New Project



Porting FREERTOS on a Different Part Number

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5. Set VIM configurations as shown in Figure 6. FreeRTOS uses 2 interrupts: RTI Compare 0 and System Software Interrput (SSI) (see Figure 7).

Enable interrupt channel: 2 - RTI Compare 0. Set ISR name as vPortPreemptiveTick Enable interrupt channel: 21 - SSI. Set ISR name as vPortYeildWithinAPI.



Figure 6. VIM Configuration

Т	MS570LS1227PG	E PINN	IUX RTI	GIC	SCI	LIN	SCI2	MIBSPI	1 MIBS	PIB	SPI4	MI
	General Drive	r Enable	SAFETY IN	IIT	R4-MP	U-PMU	Inte	rrupts	VIM Gene	ral	VIM R	AM
	VIM RAM : ISR Assignments											
	Base Address:	0xFFF82	000	1	VIM R	AM Pari	ty enabl	e				
	0.0000000.000				0.00000	0.40.45						
	0x0000000:PH	phantom	nterrupt		0x00000	040:15	adc IC	aroup linte	emupt	UXU	000008	SO:31
	0x0000004:00	esmHighl	nterrupt		0x00000	044:16	can1H	HighLevell	nterrupt	0x0	800000	34:32
	0x0000008:01	phantom	nterrupt		0×00000	048:17	phant	omInterrup	ot	0x0	800000	8:33
	0x000000C:02	vPortPree	emptive Tick		0x00000	04C:18	phant	omInterrup	ot	0x0	800000	8C:34
	0x00000010:03	rtiCompar	e1Interrupt		0x00000	050:19	crcInt	errupt		0x0	000009	0:35
	0x00000014:04	rtiCompar	e2Interrupt		0x00000	054:20	esmLo	owInterrup	t	0x0	000009	4:36
	0x0000018:05	rtiCompar	e3Interrupt		0x00000	058:21	vPort`	YeildWithir	nAPI	0x0	000009	8:37
	0x0000001C:06	rtiOverflov	w0Interrupt		0x00000	05C:22	phant	omInterrup	ot	0x0	000009	C:38

Figure 7. VIM ISR Assignments

6. FreeRTOS also uses the SVC exception. Enable the exception handler as vPortSWI, as shown in Figure 8.



Figure 8. Enable PortSWI

- 7. Disable the RTI driver and configure the other peripherals as required, then generate the code.
- 8. Copy the OS files from the 1227ZWT project to the 1227PGE project. This includes the files starting with prefix "os_", FreeRTOS.h and FreeeRTOSConfig.h.
- 9. Make the following changes in the linker command file in 1227PGE project:

ME	MORY
ſ	VECTORS (X) : origin=0x00000000 length=0x00000020 KERNEL (RX) : origin=0x00000020 length=0x00008000 FLASH0 (RX) : origin=0x00008020 length=0x00013FE0 - 0x8000 STACKS (RW) : origin=0x080001500 length=0x00001500 KRAM (RW) : origin=0x08001500 length=0x00000800 RAM (RW) : origin=0x08001500 + 0x800 length=0x0002EB00 - 0x800
/* /* }	USER CODE BEGIN (2) */ USER CODE END */
/* /*	USER CODE BEGIN (3) */ USER CODE END */
/* /*	Section Configuration
SE {	<pre>CTIONS .intvecs : {} > VECTORS /* FreeRTOS Kernel in protected region of Flash */ .kernelTEXT : {} > KERNEL .cinit : {} > KERNEL .pinit : {} > KERNEL /* Rest of code to user mode flash region */ .text : {} > FLASH0 .const : {} > FLASH0 /* FreeRTOS Kernel data in protected region of RAM */ .kernelBSS : {} > KRAM .kernelHEAP : {} > RAM .data : {} > RAM</pre>
/* /* }	USER CODE BEGIN (4) */ USER CODE END */

10. The FreeRTOS port is now ready for the TMS570LS1227PGE device.

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