

PMIC for Powering AM62Px Devices



This product overview shows the Power Management IC (PMIC) to power applications using the AM62Px Sitara™ processors. There are multiple orderable part numbers (OPNs) in the TPS6522x-Q1 family. The specific full orderable shown in the OPN row on [PMIC for Powering AM62Px Devices](#) was programmed with unique One Time Programmable (OTP) settings and optimized to support the power, sequence, and low-power mode requirements of the AM62P. This document also highlights the resources available to support new designs. The PMIC documentation and the AM62P starter kit can be used as a guide for integrating the PMIC into a system powering the AM62P Sitara™ processor.

PMIC for Powering AM62Px Devices

Parameter	TPS6522x-Q1
OPN	TPS6522430RAHRQ1
Regulators	4 DC/DC regulators, 3 LDOs
Package	5mm × 6mm 36 pin, VQFN-HR
Rating	Automotive Functional Safety, ASIL-B targeted ⁽¹⁾
Temperature Range	T _A = –40°C to 125°C T _J = –40°C to 150°C
PMIC documentation	TPS65224-Q1 Power Management IC (PMIC) with 4 Buck converters and 3 LDOs
EVM	AM62P starter kit for low-power Sitara™ processors

(1) Documentation to aid ISO26262 system design available upon product release.

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